

Refine Search

Search Results -

Term	Documents
MEMORY	1586073
MEMORIES	188296
MEMORYS	195
CONTROL	6201737
CONTROLS	1465437
CIRCUIT	3935091
CIRCUITS	1190617
SUBSTRATE	1853234
SUBSTRATES	422465
(2 AND (MEMORY ADJ CONTROL ADJ CIRCUIT) AND SUBSTRATE).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	42
(L2 AND MEMORY ADJ CONTROL ADJ CIRCUIT AND SUBSTRATE).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	42

Database: US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search: L19

Search History

DATE: Monday, January 31, 2005 [Printable Copy](#) [Create Case](#)

Set
Name Query
 side by
 side

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

Hit
Count Set
Name
 result set

<u>L19</u>	L2 and memory adj control adj circuit and substrate	42	<u>L19</u>
<u>L18</u>	L2 and memory adj control adj circuit same substrate	4	<u>L18</u>
<u>L17</u>	L2 and memory adj control adj circuit sme substrate	0	<u>L17</u>
<u>L16</u>	L2 and memory adj control adj circuit near4 substrate	0	<u>L16</u>
<i>DB=PGPB, USPT; PLUR=YES; OP=ADJ</i>			
<u>L15</u>	L2 and memory adj control adj circuit same substrate	4	<u>L15</u>
<u>L14</u>	L2 and memory adj control adj circuit and substrate	42	<u>L14</u>
<u>L13</u>	L2 and memory adj control adj circuit same substrate	0	<u>L13</u>
<u>L12</u>	L9 and (memory or storage) same control	34	<u>L12</u>
<u>L11</u>	L9 and (memory or storage) adj control	0	<u>L11</u>
<u>L10</u>	L9 and (memory or storage) adj control adj circuit	0	<u>L10</u>
<u>L9</u>	L2 and data adj line\$1 same (memory or storage) same substrate\$1	89	<u>L9</u>
<u>L8</u>	L2 and data adj line\$1 same (memory or storage) and substrate\$1	489	<u>L8</u>
<u>L7</u>	L2 and data adj line\$1 same (memory or storage)	1395	<u>L7</u>
<u>L6</u>	L2 and data adj line\$1	3798	<u>L6</u>
<u>L5</u>	L2 and data line adj driver\$1	75	<u>L5</u>
<u>L4</u>	L2 and data adj line adj driver\$1 same (memory or storage) adj control	0	<u>L4</u>
<u>L3</u>	L2 and data adj line adj driver\$1 same (memory or storage) same (memory or storage) adj control	0	<u>L3</u>
<u>L2</u>	L1 and @py<=2000	47431	<u>L2</u>
<u>L1</u>	(LCD or liquid adj crystal adj display\$1)	134246	<u>L1</u>

END OF SEARCH HISTORY

Freeform Search

Database:	<input checked="" type="checkbox"/> US Pre-Grant Publication Full-Text Database <input checked="" type="checkbox"/> US Patents Full-Text Database <input type="checkbox"/> US OCR Full-Text Database <input type="checkbox"/> EPO Abstracts Database <input type="checkbox"/> JPO Abstracts Database <input type="checkbox"/> Derwent World Patents Index <input type="checkbox"/> IBM Technical Disclosure Bulletins
Term:	L10 and @py<=2000
Display:	<input type="text" value="20"/> Documents in <u>Display Format:</u> <input type="text" value="TI"/> Starting with Number <input type="text" value="1"/>
Generate:	<input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image

Search History

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Set Name Query
side by side

Hit Count Set Name
result set

DB=PGPB,USPT; PLUR=YES; OP=ADJ

<u>L11</u>	L10 and @py<=2000	77	<u>L11</u>
<u>L10</u>	L3 and (memory or storage) adj control\$3 same substrate	182	<u>L10</u>
<u>L9</u>	L6 and @py<=2000	43	<u>L9</u>
<u>L8</u>	L6	140	<u>L8</u>
<u>L7</u>	L6	140	<u>L7</u>
<u>L6</u>	L3 and (memory or storage) control adj circuit and substrate	140	<u>L6</u>
<u>L5</u>	L4 and @py<=2000	4	<u>L5</u>
<u>L4</u>	L3 and (memory or storage) control adj circuit same substrate	15	<u>L4</u>
<u>L3</u>	(LCD or liquid adj crystal adj display\$1)	134246	<u>L3</u>
<u>L2</u>	L1	82091	<u>L2</u>
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<u>L1</u>	(LCD or liquid adj crystal adj display\$1)	82091	<u>L1</u>

END OF SEARCH HISTORY